

FIG. 1

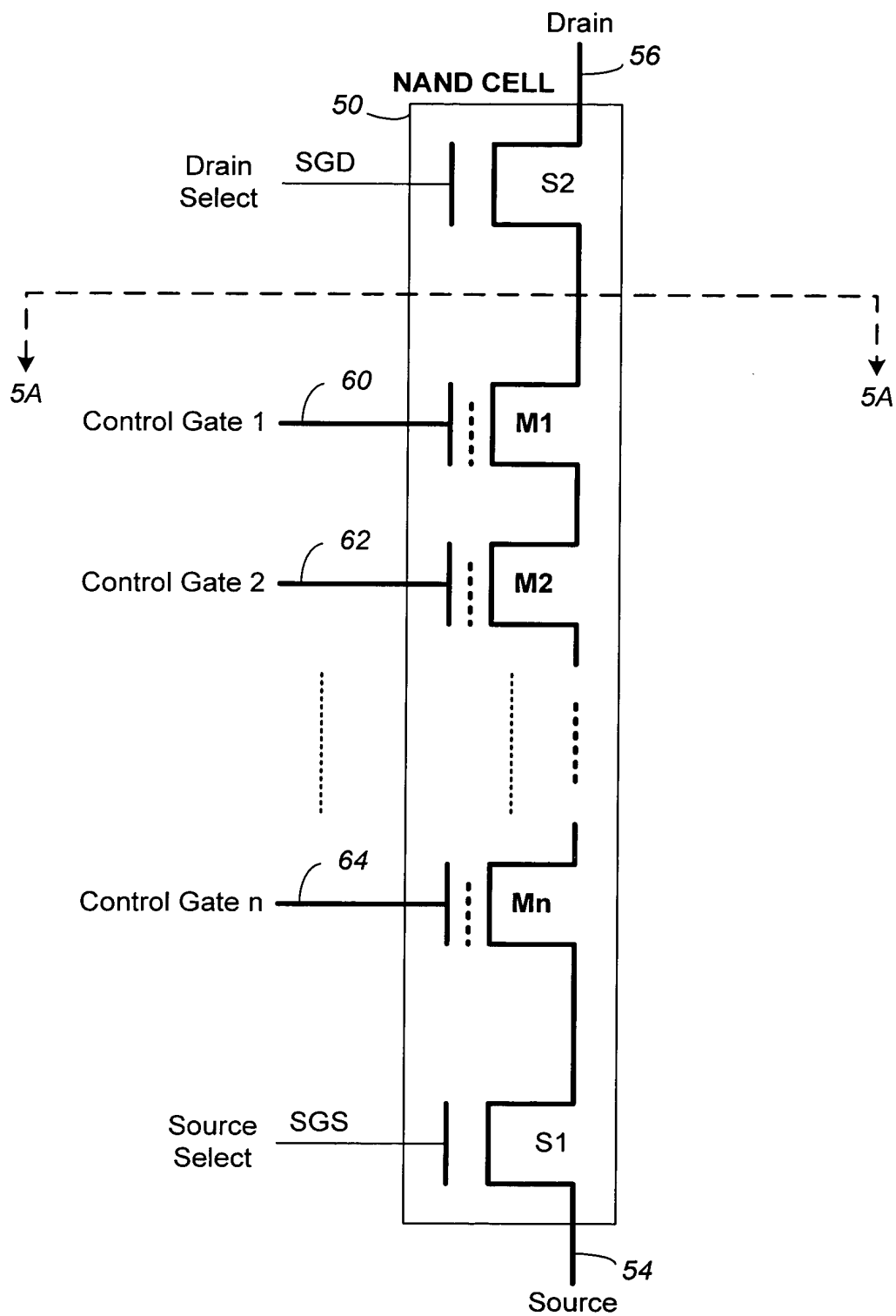


FIG. 2

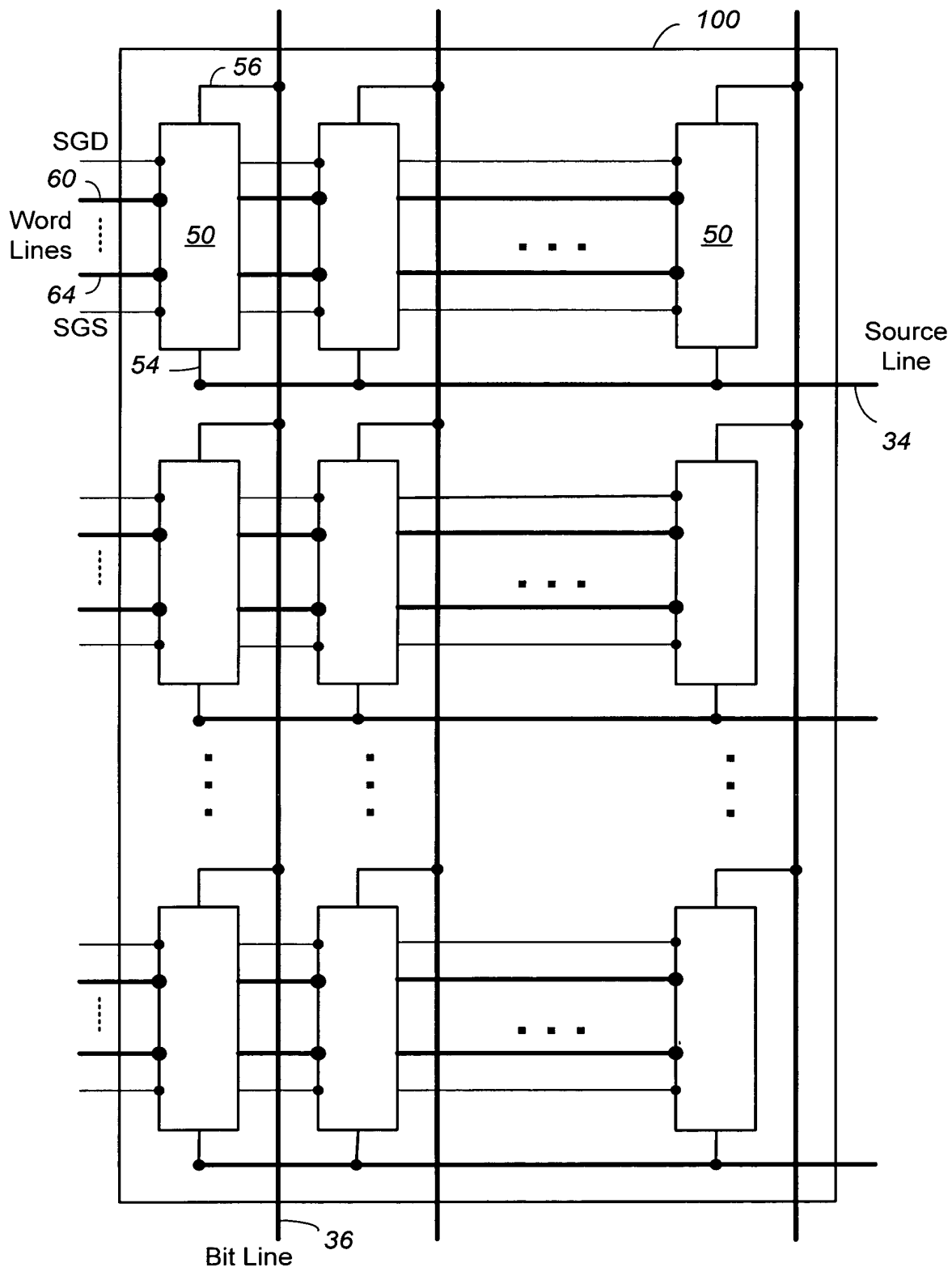


FIG. 3

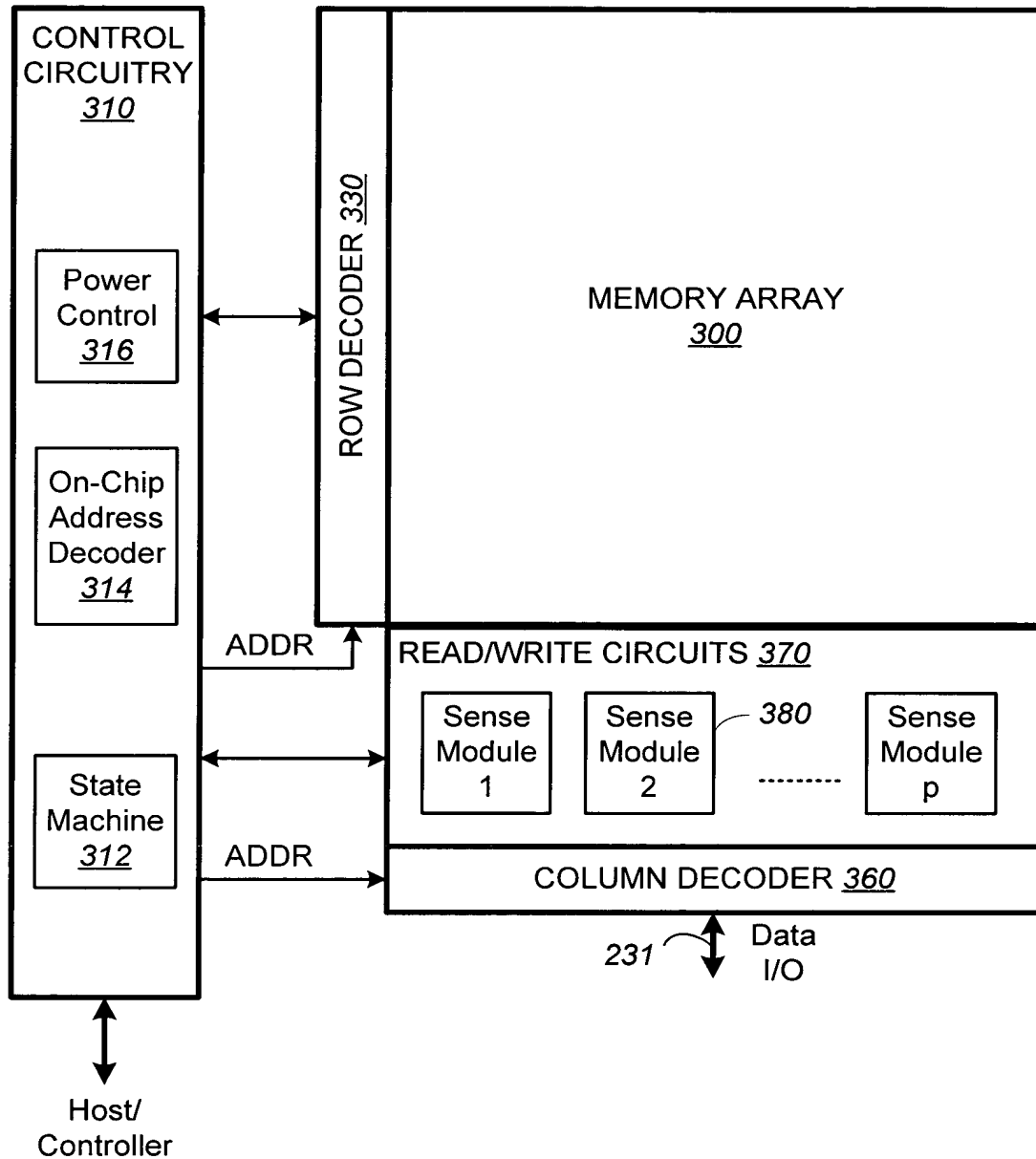


FIG. 4A

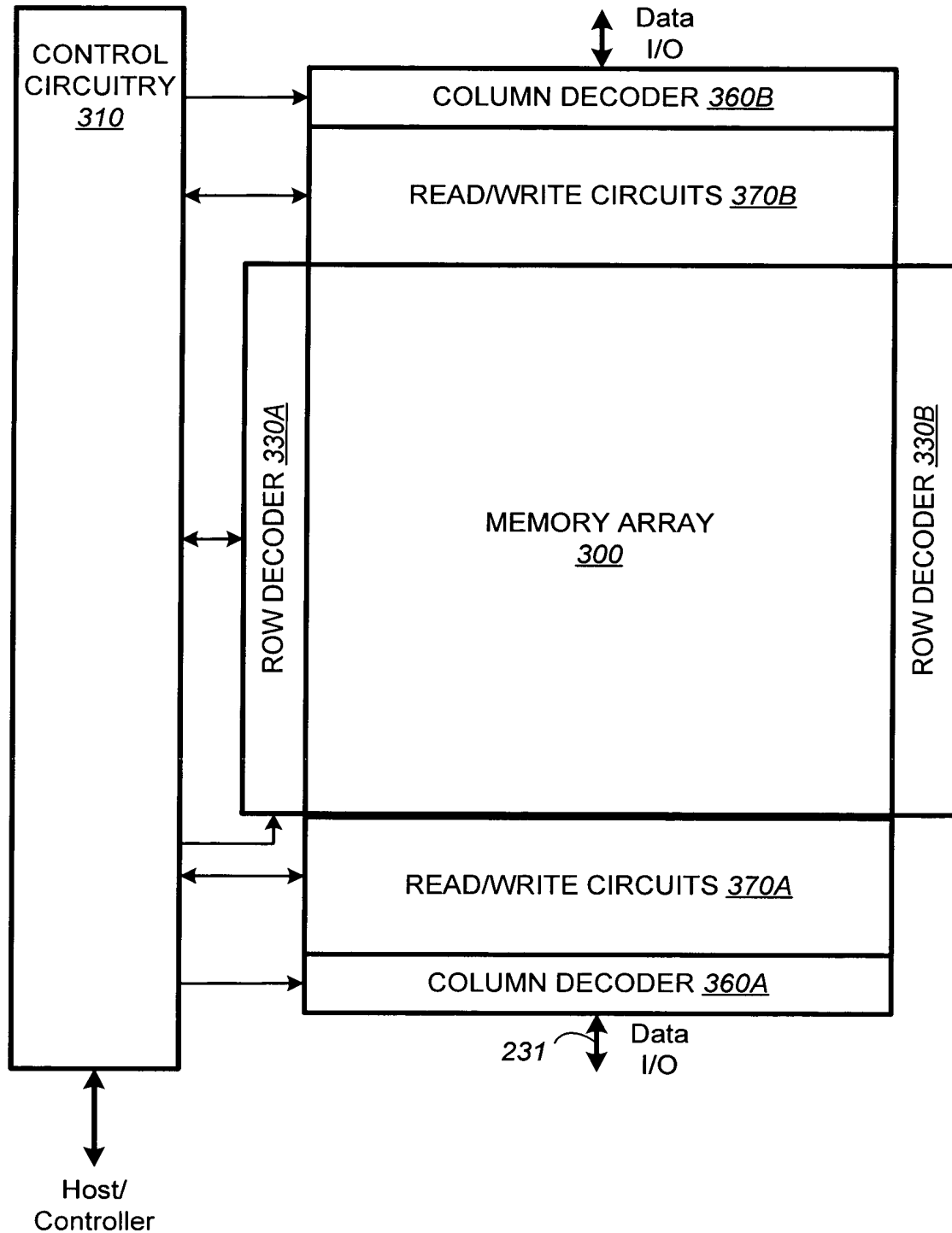


FIG. 4B

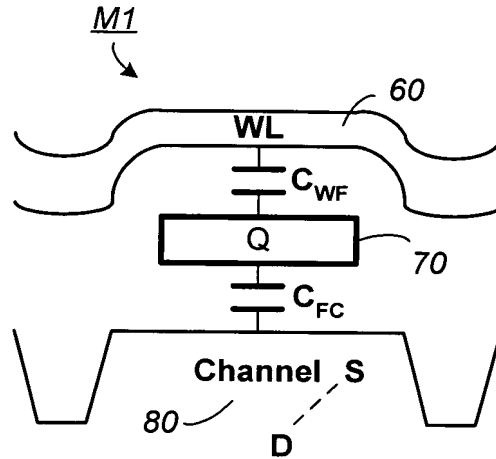


FIG. 5A

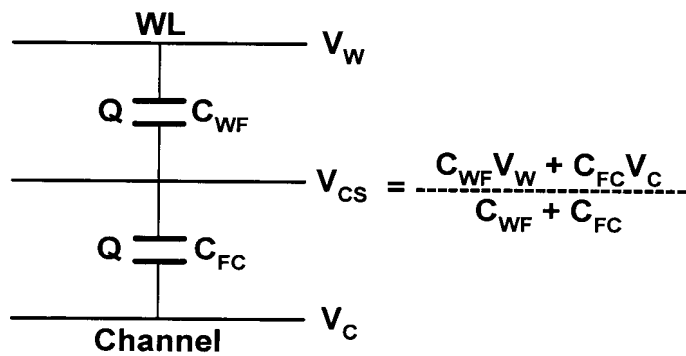


FIG. 5B

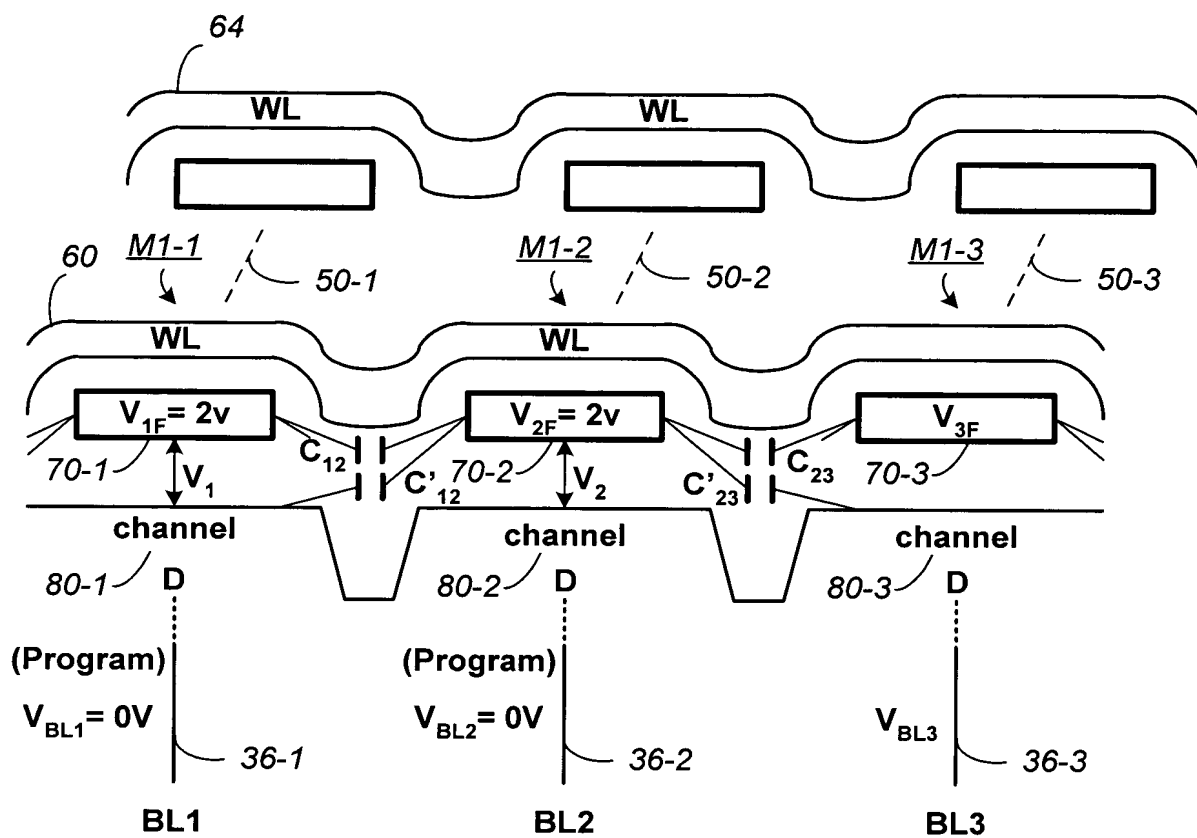


FIG. 6A

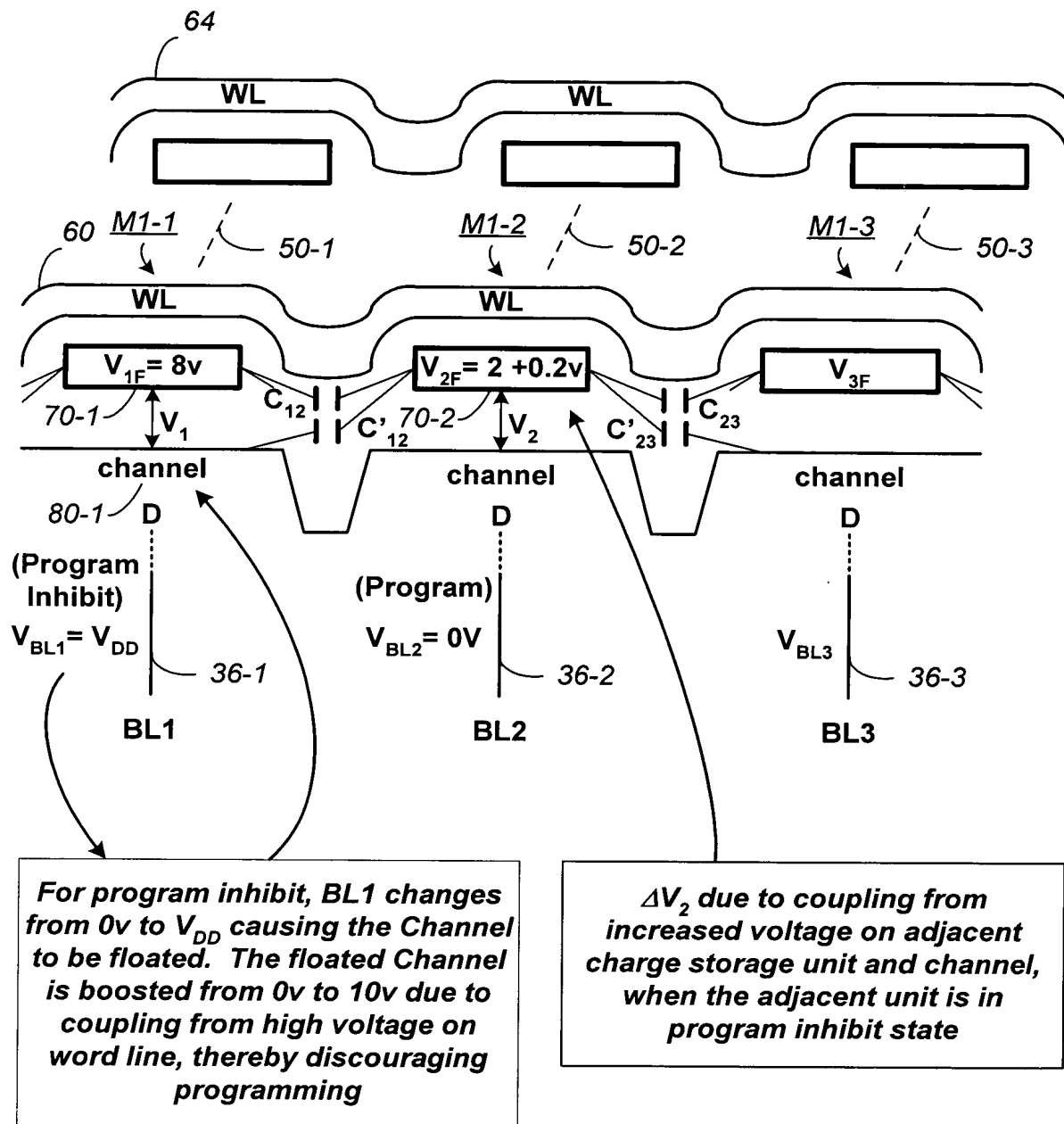


FIG. 6B

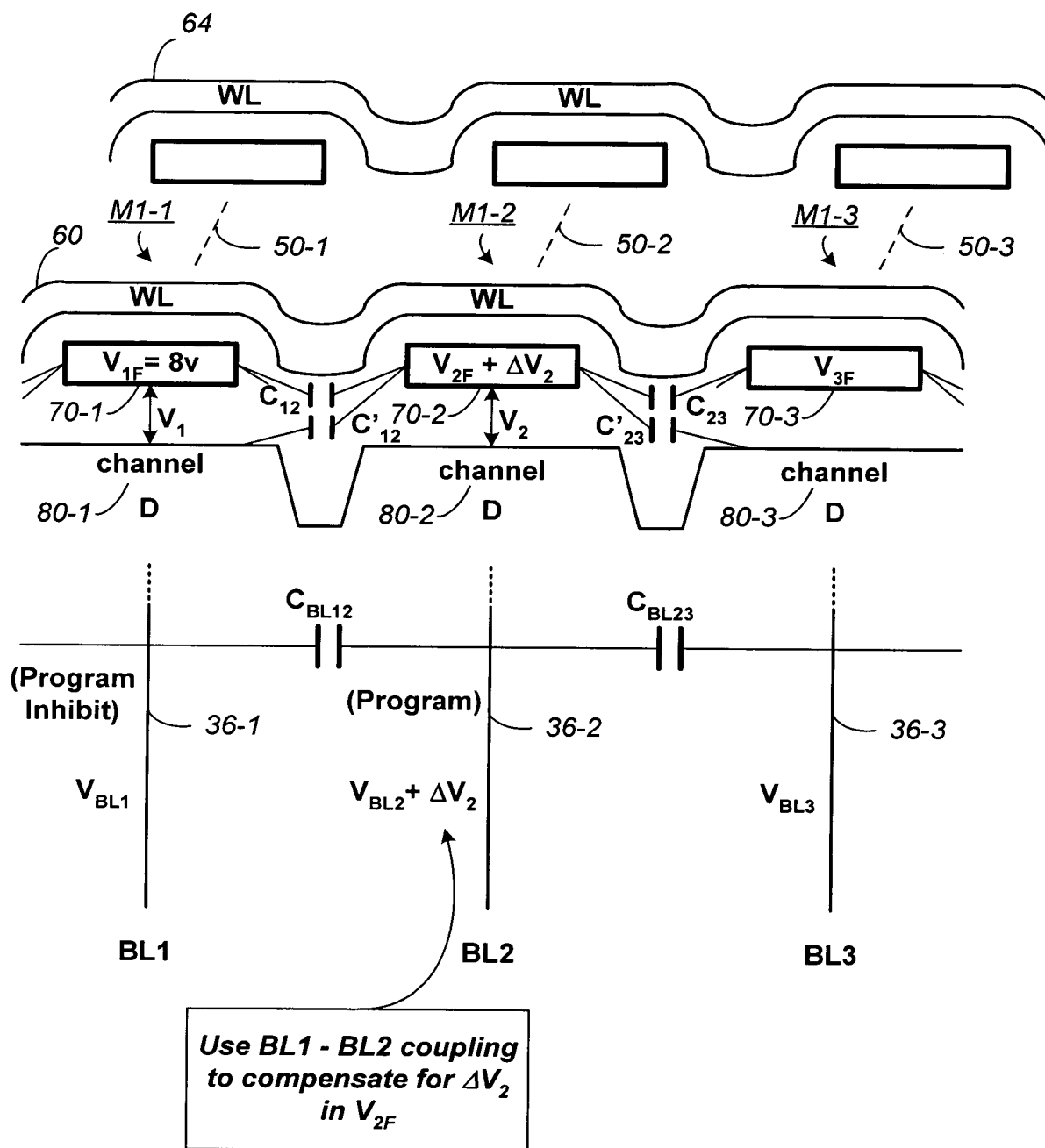


FIG. 7

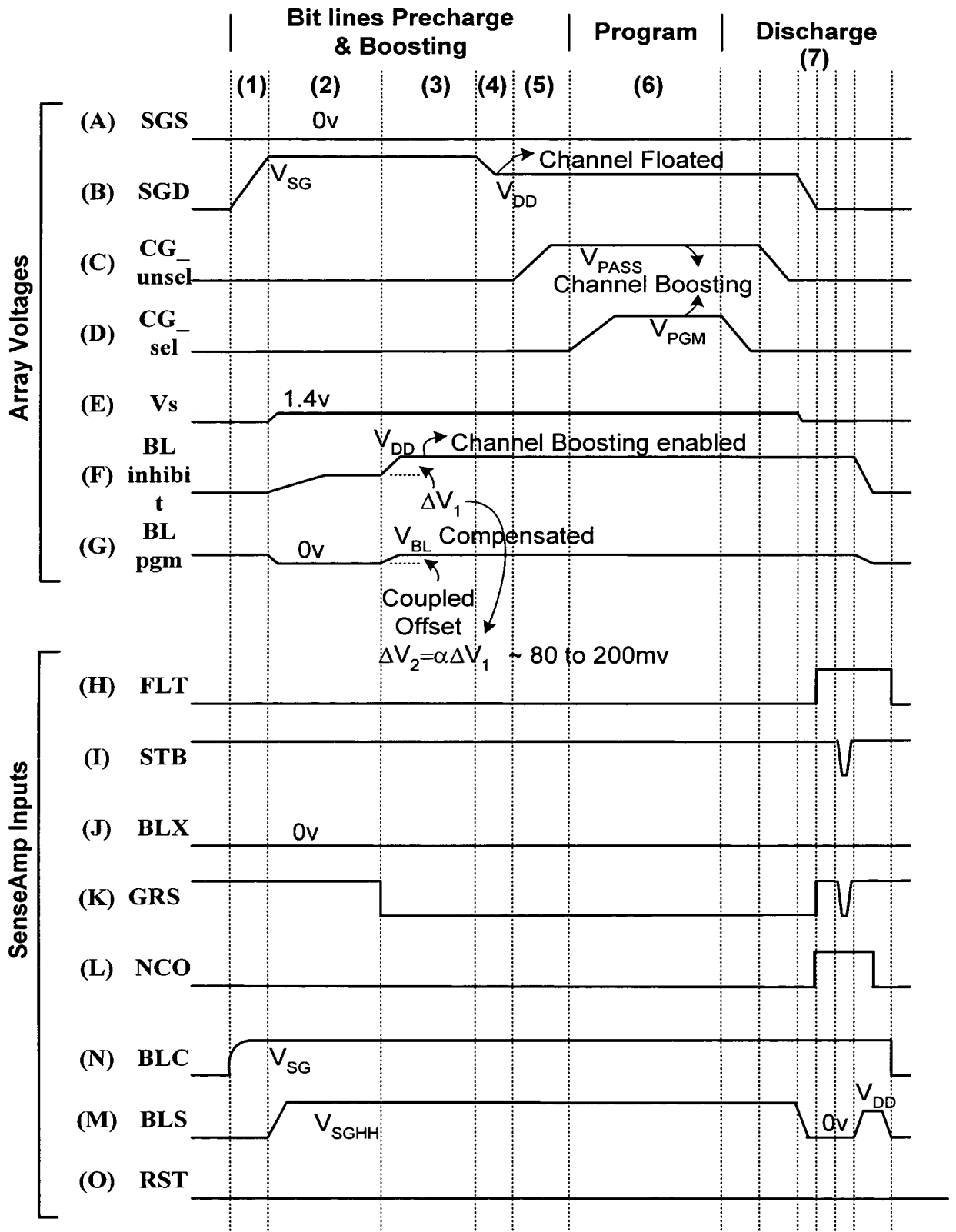


FIG. 8

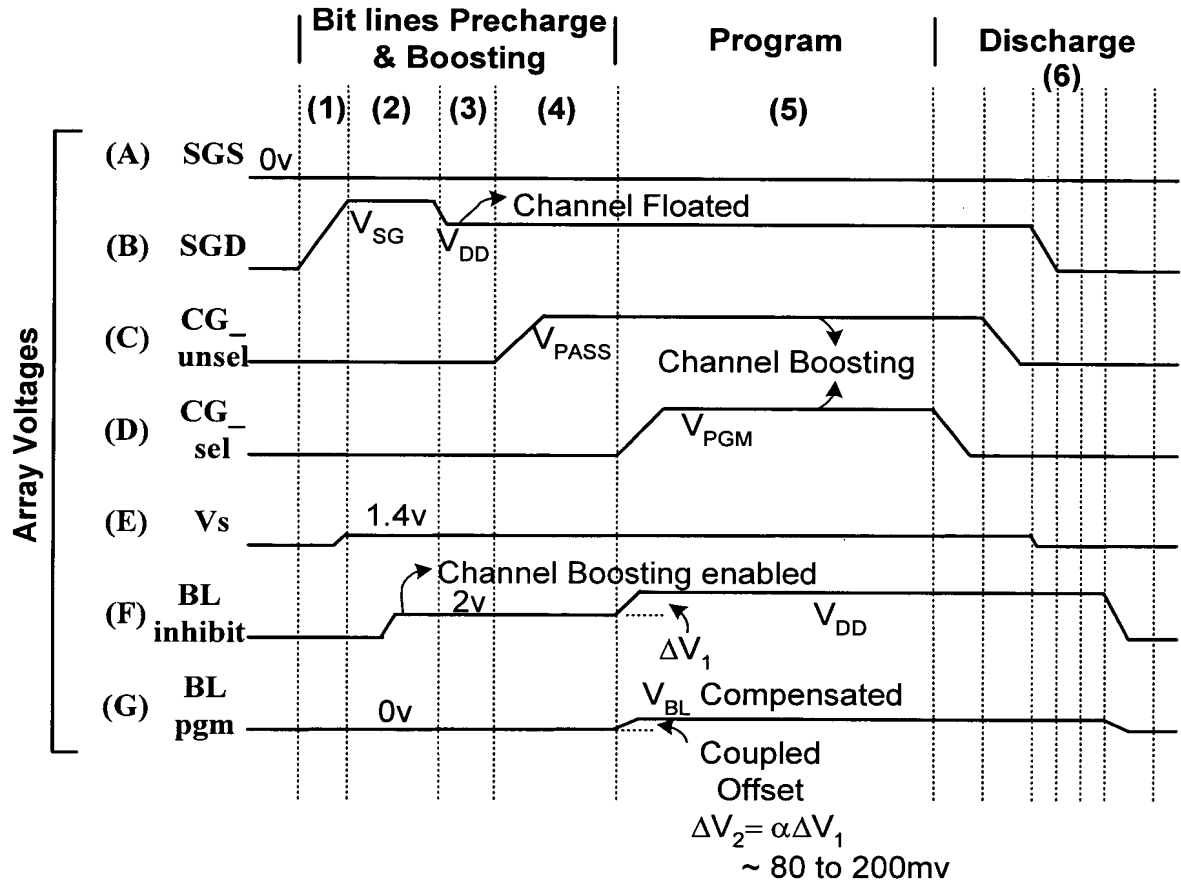


FIG. 9

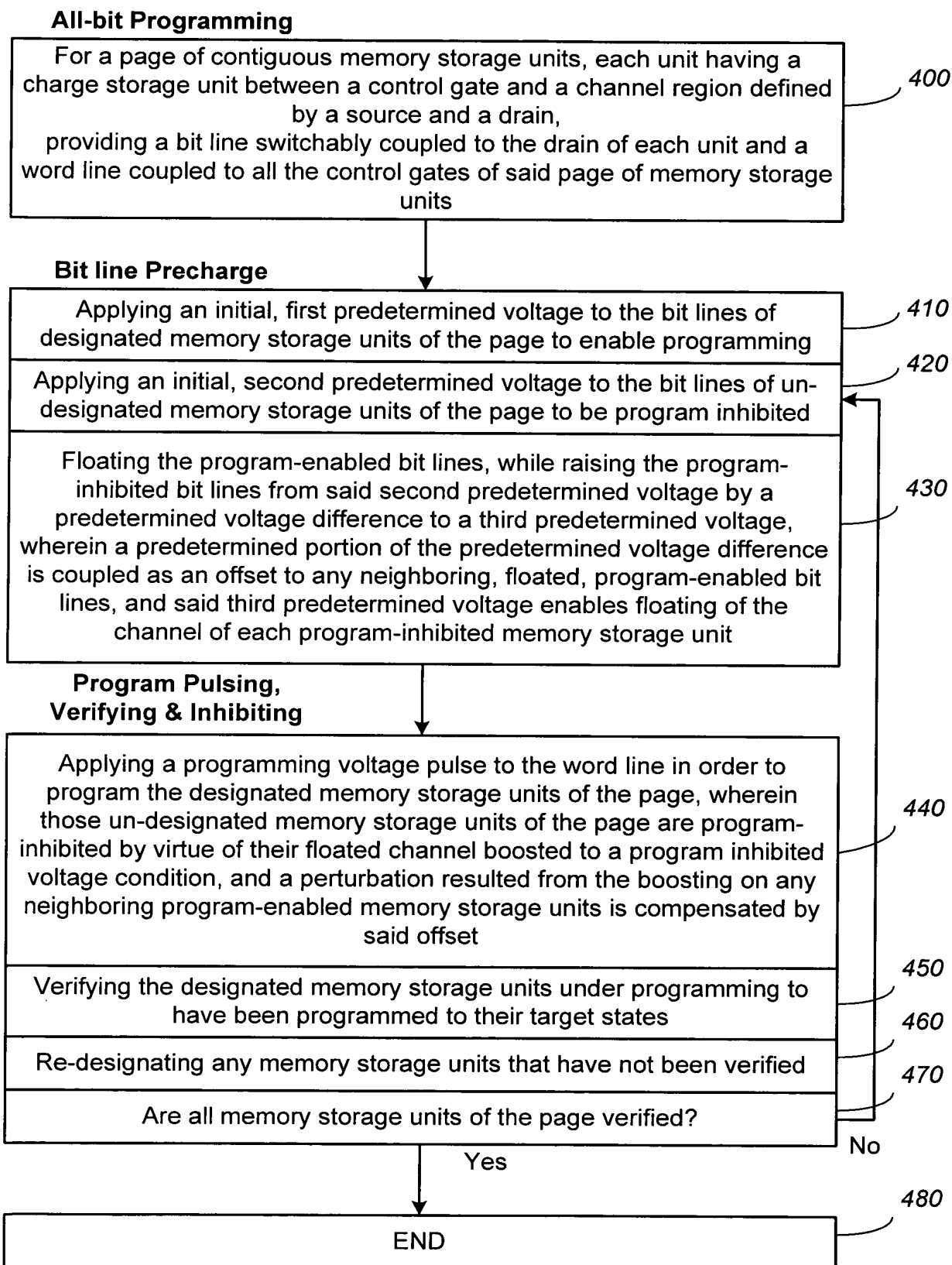


FIG. 10

Bit line Precharge (2nd Embodiment)

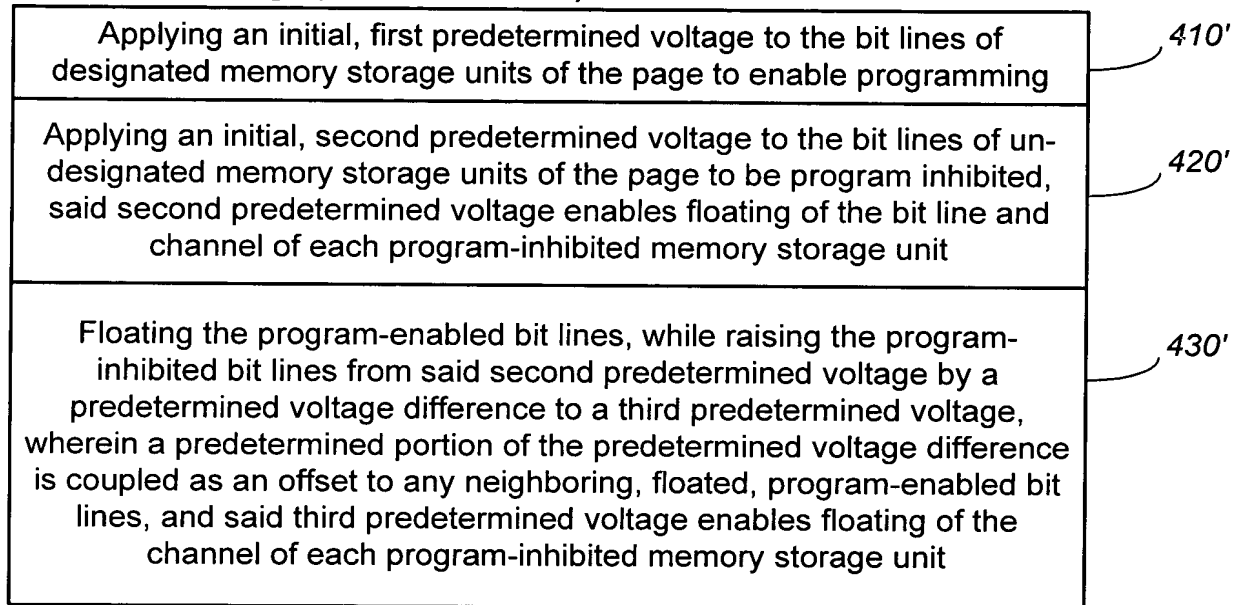


FIG. 11

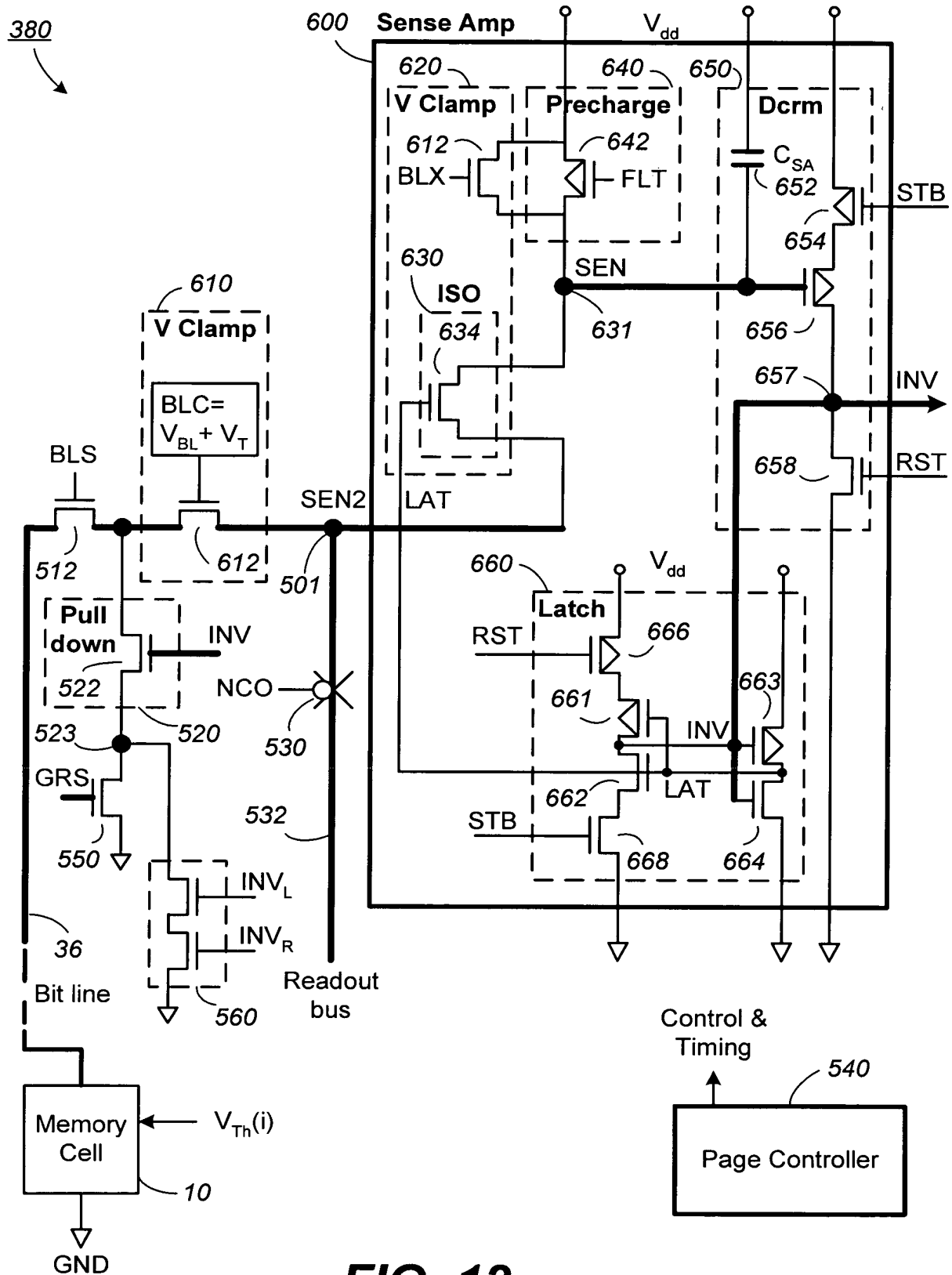


FIG. 12

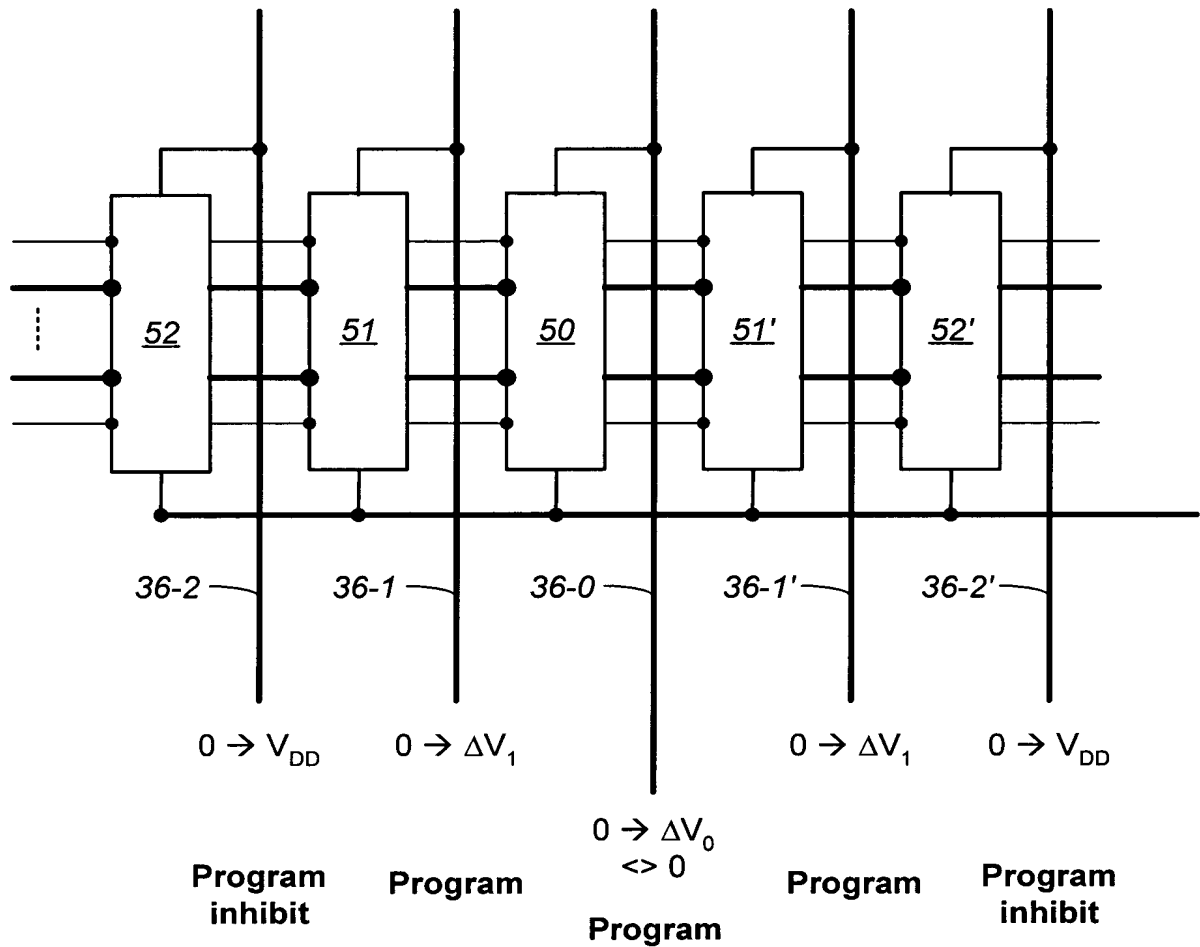


FIG. 13

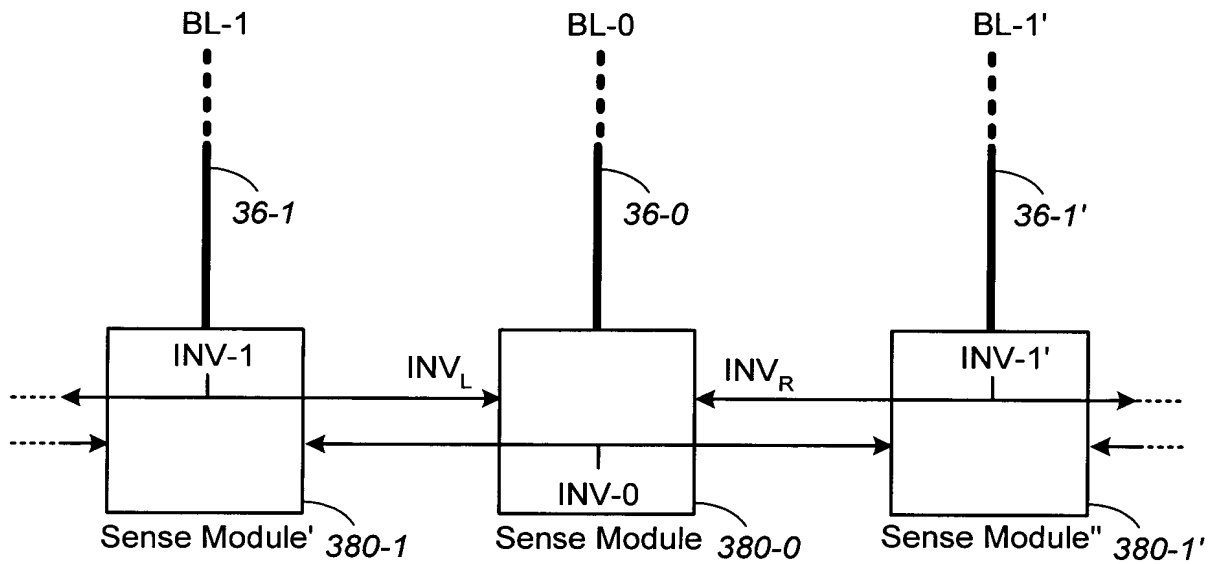


FIG. 14

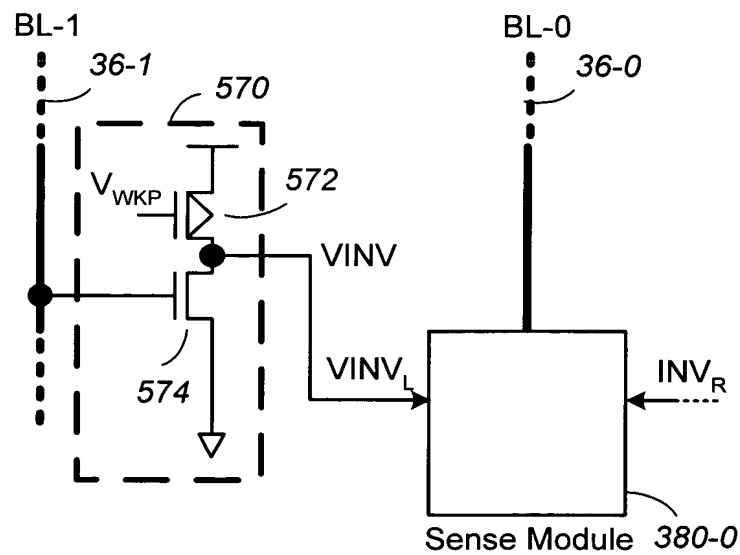


FIG. 15